

REMARKS

Pending Claims

Claims 1-11 and 14-15 have been amended. Claims 1-16 are currently pending.

Priority

Applicants appreciate the Examiner's acknowledgment of the claim for priority. Submitted herewith is the certified copy of the priority document, JP 2002-303920. An indication that the priority document has been received would be appreciated.

Information Disclosure Statement

Applicants appreciate the Examiner's acknowledgment of the Information Disclosure Statement filed on October 10, 2003.

Drawings

The drawings have been objected to for failing to disclose all of the features claimed by Applicants. In particular, the Office Action mentions claims 1 and 6 as

setting forth the features that are not shown in one of the drawings. However, Applicants request reconsideration of this objection for the following reasons.

Fig. 3 shows all of the elements set forth in claim 1. In particular, a power amplification circuit TR3 is shown and discussed in the specification as being provided for power amplification in the final amplification stage. Additionally, a transistor TR4 is provided that receives a signal that is input to transistor TR3. Further, transistors TR5 and TR6 make up a current mirror circuit. A bias generating circuit is identified by the label BIAS in the lower lefthand side of the drawing and a capacitive element C3 is shown coupled between the output terminal of transistor TR3 and a control terminal of transistor TR5. With respect to claim 6, a comparator circuit CMP is shown in Fig. 3. The bias generating circuit and the comparing circuit are different circuit elements, and therefore claim 6 has been amended accordingly.

Specification

Applicants have amended the specification to correct the inadvertent use of the word capacitative and to replace each occurrence with the word capacitive, as required. Accordingly, the specification objection should be overcome.

35 U.S.C. §112 and Claim Objections

Applicants have amended the claims to overcome the claim objections noted in the Office Action. Further, claims 1 and 2 have been amended to delete the reference to "the transmission source", "the drain" and "said transistor", respectively.

Claim 6 has been amended to clarify that the comparing circuit is not part of the bias generating circuit, and therefore the 35 U.S.C. §112, second paragraph rejection should be withdrawn.

Conclusion

In view of the foregoing amendments and remarks, Applicants contend that the above-identified application is now in condition for allowance. Accordingly, reconsideration and reexamination are respectfully requested.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "John R. Mattingly", with a stylized flourish at the end.

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